



501.39474X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Hiroyuki MAKANO et al.
Serial No.: 09/760,704
Filed: January 17, 2001
For: **METHOD OF PROCESSING A SEMICONDUCTOR DEVICE**
Group: 2851
Examiner: Andrew T. Sever

AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

August 18, 2003

Sir:

In response to the Office Action (Paper No. 9) dated on April 17, 2003, please amend the above-identified application as follows.

Amendments to the Substitute Specification begin on page 2 of this paper.

Amendments to the Claims begin on page 4 of this paper.

Amendments to the Drawings begin on page 13 of this paper and include both an attached replacement sheet and an annotated sheet showing changes.

Remarks/Arguments begin on page 14 of this paper.

An **Appendix** including amended drawing figures is attached following page 19 of this paper.